

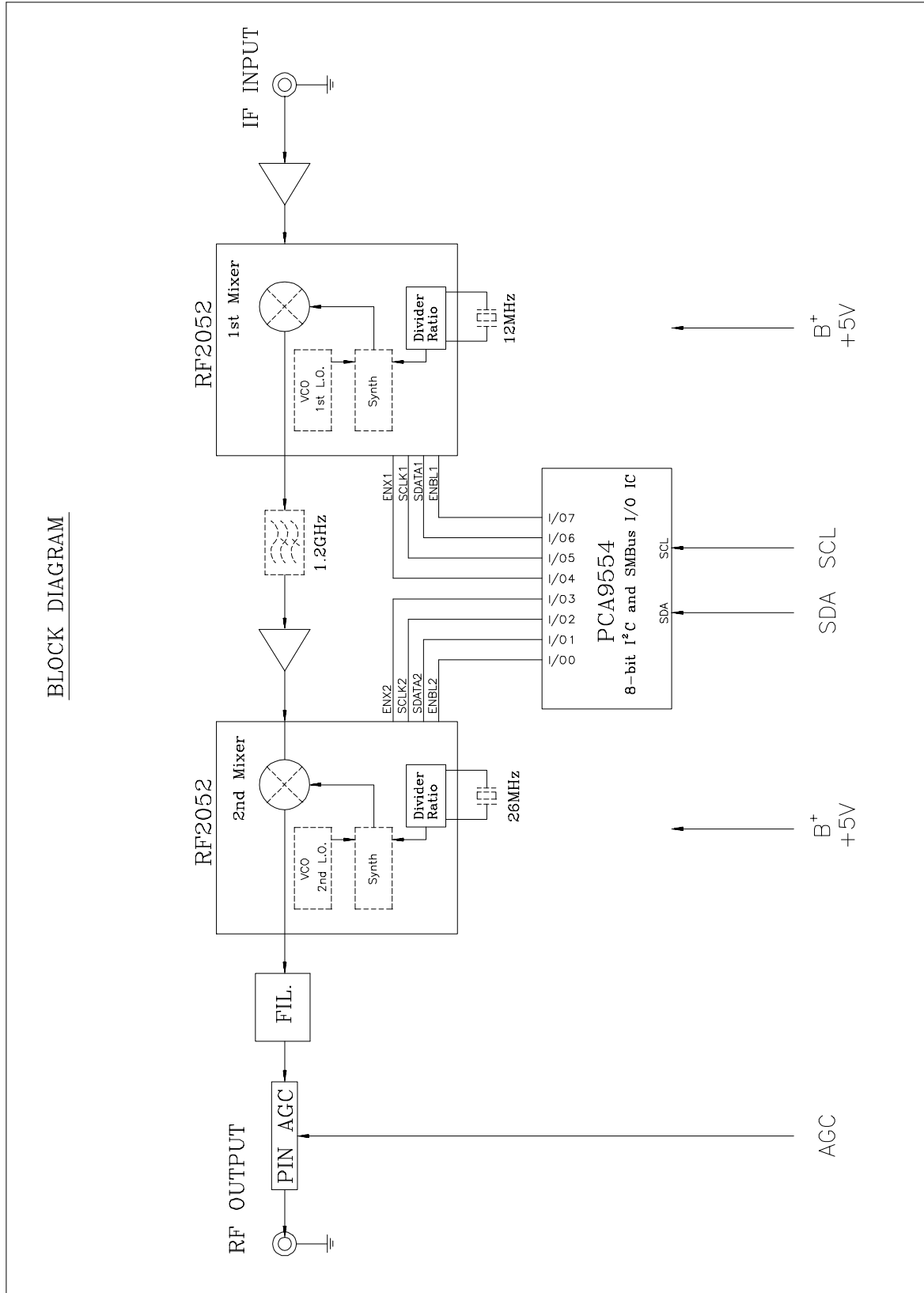
SPECIFICATION

MTCATVR UPCONVERTER MODULE

Revision:1.0

5. BLOCK DIAGRAM

BLOCK DIAGRAM





SPECIFICATION						
MTCATVR UPCONVERTER MODULE					Revision:1.0	
6. Electric Characteristics (At The Temperature Of 25°C)						
NO.	I T E M	Specification				
		MIN.	TYP.	MAX	UNIT	
6.1	Input Frequency	36		45.75	MHz	
6.2	Input Level At Fp Carrier	-28	-25	-22	dBm	
6.3	Intermediate Frequency (IF)	1218			MHz	
	Digital	1220				
	Analog					
6.4	Output Frequency (Refer To Appendix)	48		860	MHz	
6.5	1st L.O. Frequency	1172.25		1182	MHz	L.O = IF-input freq
	(PLL Control)	1174.25		1184		
6.6	2nd L.O. Frequency (PLL Control)	1266		2080	MHz	
6.7	Power Gain (At Fp Carrier)		2	5	dB	
6.8	Noise Figure (At Fp+2.75MHz)			8	dB	
6.9	Input Impedance		75		ohm	
	VSWR		1.5	2.2		
6.10	Output Impedance		75		ohm	
	Return Loss			-6	dB	
6.11	Output Spurious					
	With Input Signal Present		-56		dBc	
6.12	Output Response			3	dB	
6.13	AGC Gain Reduction (0~5V)	8	15		dB	
6.14	PLL Phase Noise			-75	dBc/Hz	
	10KHz			-85		
	100KHz					
6.15	Consumption Current B+ +5V		200	230	mA	
6.16	RF Output Carrier Freq Error	-20	Fp	+20	KHz	
6.17	Gain Difference		6		dB	
7.0	LO1 LO2 TUNER PLL PROGRAMMING					
	Reference source code and PAC9554.RF2052 Datasheet.					

